

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent: 6,475,846

Date of Issue: November 5, 2002

Name of Patentee: Marotta et al.

Title of Invention: METHOD OF MAKING FLOATING-GATE MEMORY-CELL ARRAY
WITH DIGITAL LOGIC TRANSISTORS

PRELIMINARY AMENDMENT

October 22, 2003

Box REISSUE

Assistant Commissioner for Patents

Washington, D.C. 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with the
U.S. Postal Service as First Class Mail in an envelope addressed to:
Commissioner of Patents and Trademarks, Washington, D.C. 20231 on
October 22, 2003.

Robert N. Rountree

Robert N. Rountree, Reg. No. 39,347

Dear Sir:

Texas Instruments Incorporated is the sole assignee of U.S. Pat. No. 6,475,846, submitted herewith for Reissue. The assignment is recorded at reel/frame 7726/0724. Prior to examination of the above-referenced reissue application, please enter the following amendment.

In The Drawings

A proposed drawing amendment to Figure 1 is included on a separate paper for Examiner's approval.